

REPLY UNDER 37 C.F.R. § 1.116
EXPEDITED PROCEDURE
EXAMINING GROUP 2185

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application No.: 10/653,754
Filed: September 3, 2003
Inventor(s):
Stephan G. Meier, S. Craig
Nelson, and Gene W. Shen

Title: Low Power Way-Predicted
Cache

§ Examiner: Dillon, Samuel A.
§ Group/Art Unit: 2185
§ Atty. Dkt. No: 5500-97500

RCE
9-22-08
HKO
9/25/08

RESPONSE TO OFFICE ACTION OF
July 9, 2008

Mailstop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

This paper is submitted in response to the Office Action of July 9, 2008, to further highlight why the application is in condition for allowance.

Please amend the case as listed below.